

AMENDMENTS TO THE SPECIFICATION

Please amend the paragraph on page 1, lines 10-13 as follows:

A1 This Application claims benefit of priority based on Provisional Patent Application No. 60/211,559, entitled "Path AIS Insertion For Concatenated Payloads Across Multiple Processors," by the same inventors as this Application and filed on June 15, 2000 (~~Attorney Docket No. P-8320 US~~).

Please amend the paragraph on page 2, lines 1-4 as follows:

Provisional Patent Application No. 60/174,323, filed [] January 4, 2000, and entitled "A RESOURCE MANAGEMENT PROTOCOL FOR A CONFIGURABLE NETWORK ROUTER" having H. M. Zadikian, A. Saleh, J. C. Adler, Z. Baghdasarian and Vahid Parsi as inventors;

A2 [Please amend the paragraph on page 2, lines 5-6 as follows:]

Patent Application No. 09/477,217, filed [] January 4, 2000, and entitled "FAULT ISOLATION IN A SWITCHING MATRIX," having R. A. Russell and M. K. Anthony as inventors;

Please amend the paragraph on page 2, lines 10-12 as follows:

Patent Application No. [] 09/478,235 (~~Attorney Docket M-7271 US~~), filed herewith January 4, 2000, and entitled "A METHOD FOR PATH SELECTION IN A NETWORK," having A. Saleh as inventor;

A3 [Please amend the paragraph on page 2, lines 13-15 as follows:]

Patent Application No. [] 09/477,498 (~~Attorney Docket M-7272 US~~), filed [] January 4, 2000, and entitled "METHOD OF PROVIDING NETWORK

SERVICES," having H. M. Zadikian, S. E. Plote, J. C. Adler, D. P. Autry, and A. Saleh as inventors;

Please amend the paragraph on page 2, lines 16-19 as follows:

Patent Application No. [09/__, __] 09/608,097, filed June 30, 2000, entitled "CONCATENATION DETECTION ACROSS MULTIPLE CHIPS," to Douglas E. Duschatko, Lane Byron Quibodeaux, Robert A. Hall, and Andrew J. Thurston as inventors (~~Attorney Docket P-8321 US~~);

Please amend the paragraph on page 2, lines 20-22 as follows:

Patent Application No. [09/__, __] 09/609,577, filed June 30, 2000, entitled "FIXED ALGORITHM FOR CONCATENATION WIRING," to Vahid Parsi and Andrew J. Thurston as inventors (~~Attorney Docket P-8209 US~~); and

Please amend the paragraph on page 2, lines ²²⁻²⁶20-22 as follows:

Patent Application No. [09/__, __] 09/608,461, filed June 30, 2000, entitled "CHANNEL ORDERING FOR COMMUNICATION SIGNALS SPLIT FOR MATRIX SWITCHING," to Douglas E. Duschatko, Lane Byron Quibodeaux, Robert A. Hall, Andrew J. Thurston as inventors (~~Attorney Docket P-8340 US~~).

Please amend the paragraph on page 4, lines 11-22 as follows:

The payload is then mapped into a single STS-Nc Synchronous Payload Envelope (SPE) for transport. Network equipment supporting the multiplexing, switching or transport of STS-Nc SPEs treat an STS-Nc SPE as a single entity. When an STS-Nc SPE is treated as a single entity, concatenation indicators are present in the second through the Nth STS payload pointers. The pointers indicate that the STS-1s in the STS-Nc are linked. The concatenation indicators do not, however, indicate when a concatenated STS-Nc is spread across multiple pointer processors ASICs. Accordingly, it is desirable to have a system and method for detecting a concatenated STS-Nc payload that is distributed among multiple pointer processor ASICs. Such a system and method is described in U.S. Patent Application No. [_____] 09/608,097, entitled

a4 "CONCATENATION DETECTION ACROSS MULTIPLE CHIPS", filed on the same date as this Application (~~Attorney Docket No. M-8321-US~~), and hereby incorporated in its entirety into this Application.

Please amend the paragraph on page 7, lines 6-11 as follows:

a5 Fig. 1A illustrates a router 100. Router 100 includes an input/output section 110, a node controller 120, and a switching matrix 130. Node controller 120 contains, for example, real time software and intelligent routing protocols (not shown). Router 100 supports interfaces including, but not limited to, optical signal interfaces 140 (e.g., SONET), a user interface module 150, and a management system 160. Internal input signals 170 and internal output signals 180 may be electrical or optical in nature.
